

Applicant: Jae-Hun Kim and Kyu-Whan Chong  
Serial No. not yet assigned  
Filing Date: September 8, 2003      Group: not yet assigned  
Title: MOS TRANSISTOR HAVING A RECESSED GATE ELECTRODE AND  
FABRICATION METHOD THEREOF

**INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)**

**U.S. PATENT DOCUMENTS**

<u>Exa</u> <u>m</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
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**FOREIGN PATENT DOCUMENTS**

<u>Exa</u> <u>m</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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**OTHER DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>

Examiner: \_\_\_\_\_

Date Considered: \_\_\_\_\_